SEMINAR ANNOUNCEMENT

DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING
Faculty of Engineering
Website: https://www.eng.nus.edu.sg/ece/

Area: Microelectronic Technologies & Devices

Host: Assoc Prof Ang Kah Wee

TOPIC	**:**	**Introduction to School of Microelectronics / SUSTech**
SPEAKER	**:**	**Prof Yu Hongyu,** **Dean of SUSTech-HKUST School of Microelectronics**
DATE	**:**	**1 October 2019, Tuesday**
TIME	**:**	**10am to 11am**
VENUE	**:**	**E5-02-32, Engineering Block E5, Faculty of Engineering, NUS**

ABSTRACT

Riding on the complementary research strengths of both SUSTech and HKUST, a Joint School of Microelectronics has been established to become a multilateral collaborative platform for industry, academia and research at the national level. The Joint School aims to facilitate both nurturing of market-desired high-calibre talents and innovative research in key strategic areas including integrated circuit (IC), system-on-chip design for communications, artificial intelligence and robotics, as well as next generation microelectronics device technology such as wide bandgap semiconductors.

Under the establishment, the Joint School will be situated on the SUSTech campus. It will offer education programs at both the undergraduate and postgraduate levels, and run four research centres on IC design methodology and Electronic Design Automation (EDA); communications; system-on-chip design and system integration; as well as wide bandgap semiconductor devices. In this talk, an introduction to SUSTech and the opportunities available in the Joint School of Microelectronics would be given.

BIOGRAPHY

Hongyu Yu, **Professor and Dean of School of Microelectronics and Deputy Director of Shenzhen Institute of Wide Gap Semiconductors.** He has set up School of Microelectronics and the third generation semiconductor research institute in Shenzhen. On behalf of Southern University of Science and Technology, he set up the 5G Innovation Center of Medium and High Frequency Device Manufacturing Industry with Tsinghua University. In collaboration with University of Hong Kong, University of Macau and Hong Kong University of Science and Technology, he enrolled the first experimental class and established Shenzhen third-generation semiconductor key laboratory, Guangdong province GaN device engineering technology center, and served as director. He has been awarded with the "1000 Talents Program for Young Scholars", Peacock project Shenzhen, Pengcheng scholar, Fellow of IET, special government allowance, deputy editor of Science Bulletin (China's top comprehensive academic journal) and editor of Journal of Semiconductor.

He received his BSc, MSc, and PhD from Tsinghua University, University of Toronto, and National University of Singapore, respectively. He is a senior researcher in IMEC Belgium 2004 to 2008. From 2008 to 2011, he served as an assistant professor in the School of Electrical and Electronic Engineering, Nanyang Technological University, Singapore.

BIOGRAPHY

Professor Yu Hongyu has achieved a series of innovative work in integrated circuit technology and devices, including CMOS, new ultra-high density memory, GaN device and system integration (GaN HEMT). He has published more than 370 papers (>160 journal papers + >170 conference papers) with a SCI H-index of **38**; Written 4 book chapters, edited 2 books: “Hafnium: Chemical Characteristics, Production and Application” and “Gallium Nitride Power Devices”. Applied/authorized 20 US/European patents and more than **30 Chinese patents**. As a PI, he has undertaken ~20 research/talent projects, with a total funding ~70 million RMB. He has built a 1,200-square-metre clean room in Southern University of Science and Technology, forming a full 6-inch CMOS lab which hardware will reach the top level in southern China.