

We are a young fabless IC design company, headquartered in China. In Singapore site, our R&D team works on WiFi SoC products. We are looking for talented engineers to join our Digital Design team.

Read on for detailed job description. Interested candidates can send his/her resume to Xiaolei.Chen@supremicro.com

Digital IC design Engineer

Job Responsibilities

- Co-design the module and SoC design specification
- Responsible for the module RTL design and SoC integration
- Participate in module and SoC verification
- Responsible for RTL link check, synthesis and power analysis
- Participate in FPGA prototype verification

Job Requirements

- EE, Communication, Computes related Major
- More than 3 years experience in digital IC design and RTL coding.
- More than 1 time Tape-out IC project experience
- Familiar with frontend EDA tools: VCS/Verdi/DC/Formal
- Familiar with Perl/Python/Tcl language
- Familiar with AMBA bus protocol and ARM or RISCV based Architecture
- Prefer to have the experience below (one or more):
 - Wireless communication acceleration module design
 - SoC low power design
 - NoC tools or AMBA designer using experience
 - Familiar with one or more protocol of WIFI/Bluetooth/NB-IoT