Area: Integrated Circuits & Embedded Systems

Host: Professor Alioto, Massimo Bruno

TOPIC	:	**AI Innovations at the Edge: Unraveling Advanced Hardware and In-Memory Computing Architectures**
SPEAKER	:	**Dr Nitin Chawla** **Director, Technology R&D (Strategy and Innovation) at STMicroelectronics**
DATE	:	**Thursday, 28 September 2023**
TIME	:	**5.00PM to 6.00PM**
VENUE	:	**Block E5, E5-02-32** **College of Design and Engineering, National University of Singapore**

ABSTRACT

In this talk, the latest developments in AI technology designed for edge computing scenarios are explored. This session delves into cutting-edge hardware innovations and in-memory computing architectures, shedding light on how these advancements are shaping the future of artificial intelligence at the edge.

BIOGRAPHY

Nitin Chawla is an ST Fellow and Director in the Technology R&D (Strategy and Innovation) organization at STMicroelectronics, where he leads the research initiatives in the area of Low Power Neural Networks and In-Memory computing architectures for Edge and Tiny ML applications. Nitin has a major in Electronic Circuits and Systems. He is an alumnus of Stanford University and holds a TRIZ diploma from the Massachusetts Institute of Technology, Cambridge. He has served in different R&D and product organizations over the last 25 years. Before joining STMicroelectronics, he was the Chief Scientist of the HLS Product Division at Mentor Graphics Corporation based in Oregon, USA. Nitin has over 40+ US patents and more than 30 conference and journal publications.

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