## **IE3100 System Design Project**

## Simulation Model Development for Semiconductor Foundry

⊠ssmc

By Chen Si, Janny Yu, Jiang Lizi, Li Huiwen, Pan Xiaoshu Supervised by A/P Lee Loo Hay, Dr. Ng Tsan Sheng, Adam



**Objective**:

- To build a simulation model that reflects the basic wafer fabrication characteristics of SSMC.
- To use this simulation model to study the impact factors on cycle time

